

1/5

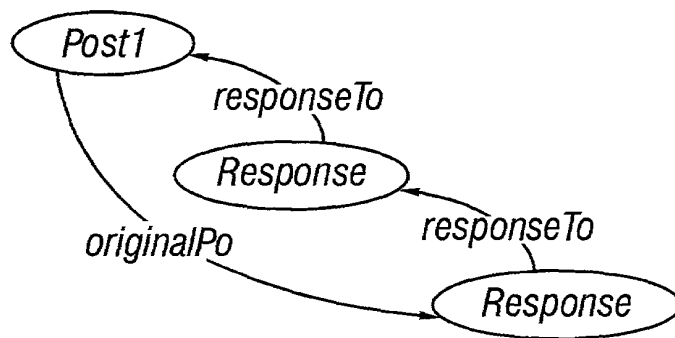


FIG. 1

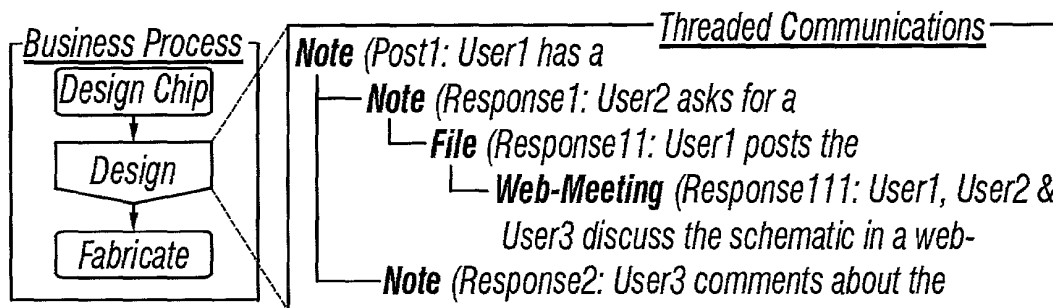


FIG. 2

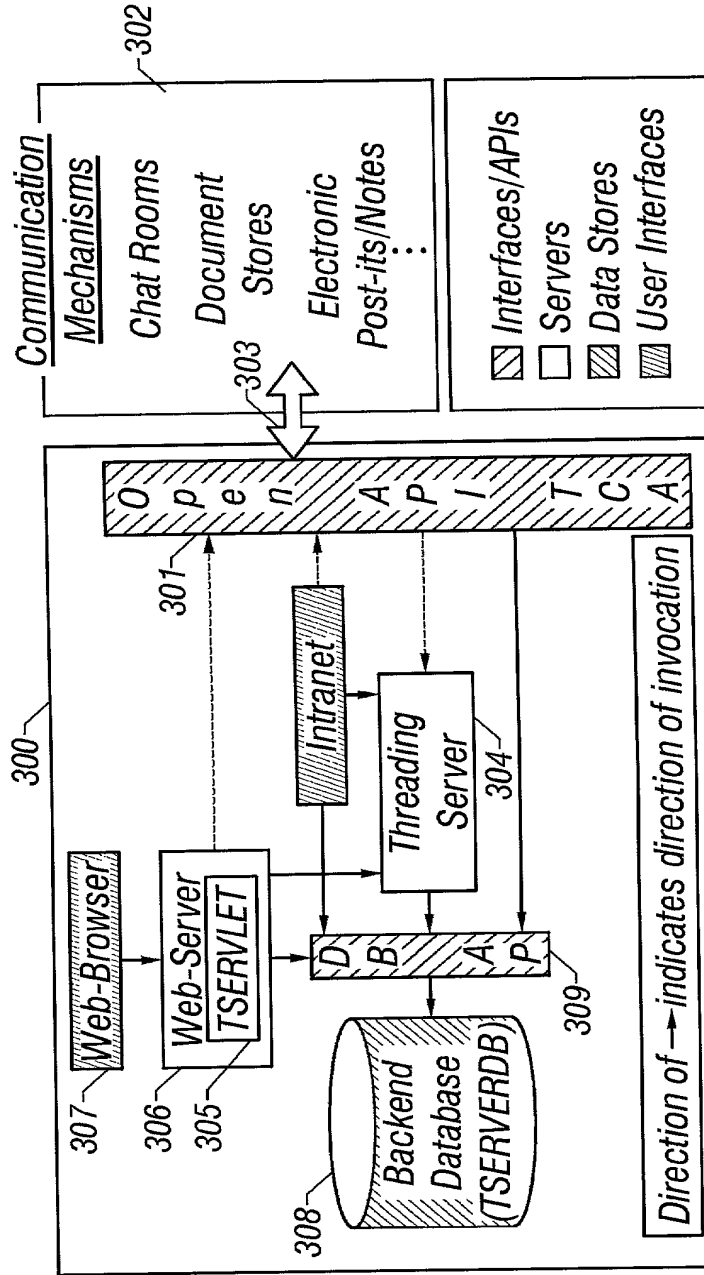


FIG. 1

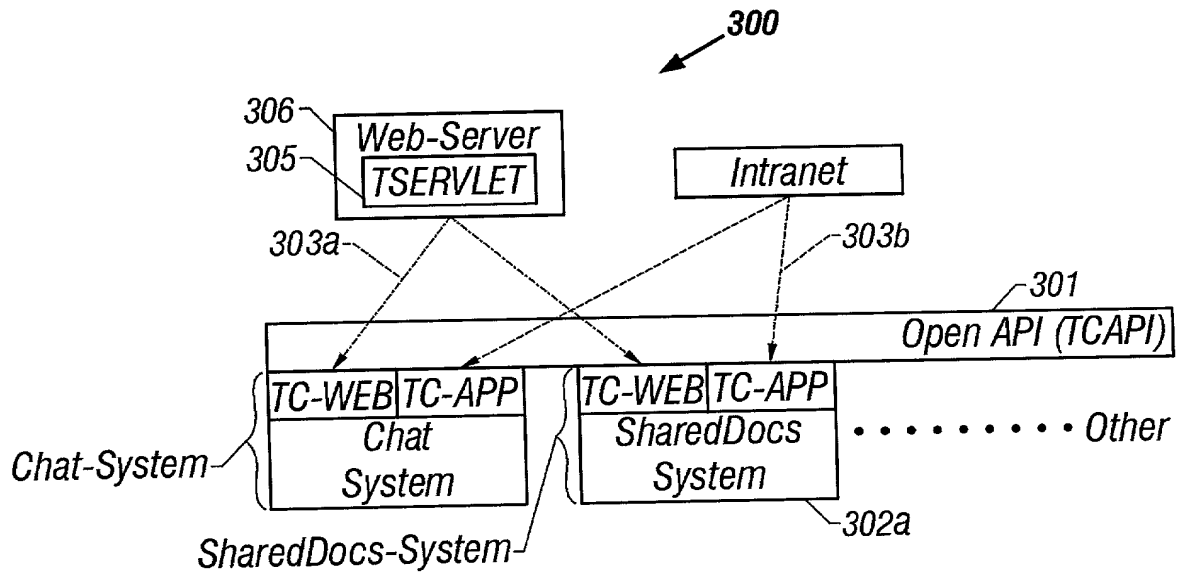


FIG. 4

500

Process

TopLevel

Detailed Reports · Design Discussions ·

Process

504

Status

Started On

Completed On

Define Chip

Design Chip

Fabricate Chip

Threaded Communications

Threaded Communications

Threaded Communications

Discussion Thread

501

Post/New Item ·

502

Title	Owned By	Created On	
Problem with Pin5	john	Dec 10 2001	Respond, Details, Delete
Show me the Schematic	chen	Dec 10 2001	Respond, Details, Delete
Here is the schematic			Respond, Details, Delete
Lets Talk!	ajit	Dec 10 2001	Respond, Details, Delete
Fanout issue?	mona	Dec 10 2001	Respond, Details, Delete

503

506

Note

Edit Note ·

Title: Fanout issue?

Do you think there is a fanout issue here?

FIG. 5









Post New PIE Item · Search · 						
Show <input type="checkbox"/> All <input type="checkbox"/>						
Title	Owned By	Created On				
 What are these errors? What are these 'Unknown pin xxx' errors from lines number 48-72?	vasant	Dec 10 2001	View Thread,	Respond,	Details,	Delete
 Synthesis Log File Hi folks- this is the log file for my Synthesis block. Any comments people?	jen	Dec 10 2001	View Thread,	Respond,	Details,	Delete
 Fanout issue? Do you think there is a fanout issue here?	mona	Dec 10 2001	View Thread,	Respond,	Details,	Delete
 Lets Talk! Let us discuss this problem	ajit	Dec 10 2001	View Thread,	Respond,	Details,	Delete
 Here is the Schematic ALU block Schematic	john	Dec 10 2001	View Thread,	Respond,	Details,	Delete
 Show me the Schematic Show me the Schematic	chen	Dec 10 2001	View Thread,	Respond,	Details,	Delete
 Problem with Pin5 I think there is some issue with the load on Pin5. The log file seems to indicate something along these lines. Any thoughts folk...	john	Dec 10 2001	View Thread,	Respond,	Details,	Delete

FIG. 6